

***DRAFT* Call For Proponents For FR-TV and RRNR-TV Objective Video Quality Model Validation Tests**

The Video Quality Experts Group (VQEG) wishes to announce that two validation tests will be conducted: one for Full Reference TV models and one for Reduced Reference-No Reference TV models. The VQEG's Independent Lab Group will conduct these tests and a fee of \$5,000.00 USD will be required from each proponent for each test in which they participate.

The deadline for announcing a proponent's intent to participate in either of the VQEG tests is April 22nd 2002. An E-mail is required to be issued to the VQEG reflector ituvidq@its.bldrdoc.gov stating your intention to participate.

FRTV Timelines

The fee payment for the FR-TV test is due August 31st, 2002 and the final submission of the model's executable software code is due on September 8th, 2002. The executables must be submitted to both Co-Chairs of the ILG by this date. The Co-Chairs of the ILG are, Filippo Speranza of the Communications Research Centre and Philip Corriveau of Intel Corporation.

RRNR-TV Timelines

The fee payment for the RRNR-TV test is due October 15th, 2002 and the final submission of the model's executable software code is due on October 15th, 2002. The executables must be submitted to both Co-Chairs of the ILG by this date. The Co-Chairs of the ILG are, Filippo Speranza of the Communications Research Centre and Philip Corriveau of Intel Corporation.

Test Plans

Further details about both tests and schedules can be found in the test plans which can be downloaded from the VQEG website at www.vqeg.org.

Inquires

Any questions that you have regarding this process can be directed to the full VQEG reflector ituvidq@its.bldrdoc.gov or to the Co-Chairs of VQEG, Philip Corriveau (Philip.j.Corriveau@intel.com), Intel Corporation, Hillsboro, Oregon U.S.A. and Arthur Webster (webster@its.bldrdoc.gov), U.S. Department of Commerce, Colorado U.S.A.